

<b>Notice of References Cited</b>	Application/Control No. 09/924,242	Applicant(s)/Patent Under Reexamination LITTLE ET AL.	
	Examiner Mohammad A Siddiqi	Art Unit 2154	Page 1 of 1

**U.S. PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-6,032,248	02-2000	Curry et al.	712/37
	B	US-6,076,156	06-2000	Pickett et al.	712/226
	C	US-5,426,769	06-1995	Pawloski, Martin B.	710/2
	D	US-5,737,548	04-1998	Yach et al.	713/600
	E	US-5,504,903	04-1996	Chen et al.	713/1
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

**FOREIGN PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

**NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Code Optimization Techniques for Embedded DSP Microprocessors, by Stan Liao, Srinivas Devdas, Kurt Keutzer, Steve Tjiang, and Albert Wang, January 1995, Proceedings of the 32nd ACM/IEEE conference on design automation
	V	
	W	
	X	

\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.